

CIRCUIT TACKLE

(Day 1 Event)

Event Representator: Parul Chaubey (EEE 3rd Year)

Event Coordinator: Udit Shah (ECE 4th Year)

Event Detail: Circuit designing competition.

Rules & Regulations:

Round 1: Elimination Round

1. Maximum 3 & minimum 2 members per team.
2. Total time allotted half hour.
3. Objective type questions based on electrical and electronics.
4. The test comprises of 30 questions with maximum marks of 30. There will be a negative marking for every wrong answer.
5. Top 4 scoring teams will be selected for the final round.

Round 2: Final Round

1. The team will have to make a circuit model.
2. Circuit model will be based on basic electronics components but if your team comprises of a member from 3rd or 4th year then the difficulty level of model will increase.
3. Total time allotted: 60min.
4. Different circuit element will be provided to all the teams.
6. After circuit designing, judges will decide the winner of 1st and 2nd position
- 7: The decision of the judges will be final.

Venue and Timing: Electrical Lab, 12:00 PM To 02: 00 PM

Requirements: 3 files with judgment sheet +6 pens

Winners: As decided by Panel.
Only 2 winners will be announced (1st and 2nd).

Prize:

2 trophies (1st) & 2 trophies (2nd) with certificate